

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In se tratent Application of:	) Group Art Unit: 2812
Hisashi OHTANI et al.	) Examiner: R. Booth
Serial No. 09/908,727	) CERTIFICATE OF MAILING I hereby certify that this correspondence is
Filed: July 20, 2001	being deposited with the United States Postal Service with sufficient postage as First Class
For: SEMICONDUCTOR DEVICE AND	) Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450,
METHOD OF MANUFACTURING	) Alexandria, VA 22313-1450, on July 5, 2005.
THE SAME	) ODE FICHED

## **RESPONSE**

Honorable Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The Official Action mailed March 2, 2005, has been received and its contents carefully noted. This response is filed within three months of the mailing date of the Official Action and therefore is believed to be timely without extension of time. Accordingly, the Applicants respectfully submit that this response is being timely filed.

The Applicants note with appreciation the consideration of the Information Disclosure Statements filed on July 20, 2001, August 27, 2001, February 19, 2002, September 20, 2002, April 17, 2003, June 2, 2004, October 14, 2004, and October 20, 2004. A further Information Disclosure Statement is submitted herewith and review and consideration thereof is requested. This IDS includes a full translation of JP 07-038113. This reference was previously submitted with a machine generated translation and the full complete human translation is now submitted for consideration by the Examiner. This translation was recently submitted in U.S. application 09/894,125, which was noted in the IDS filed October 20, 2004 in the subject application.